DIO8850B

Single Stage Buck PFC Controller for LED Lighting

Features

- **Valley turn-on of the MOSFET to achieve low switching losses**
- **0.3V current sense reference voltage leads to a lower sense resistance thus a lower conduction loss**
- **Low start up current: 15µA typical**
- **Reliable short LED and Open LED protection**
- **Power factor >0.9**
- **Compact package: SOT23-6, SOIC-8**

Descriptions

The DIO8850B is a single stage Buck PFC controller targeting at LED lighting applications. It drives the Buck converter in the quasi-resonant mode to achieve higher efficiency.

It keeps the Buck converter inconstant on time operation to achieve high power factor.

Applications

- **LED Lighting**
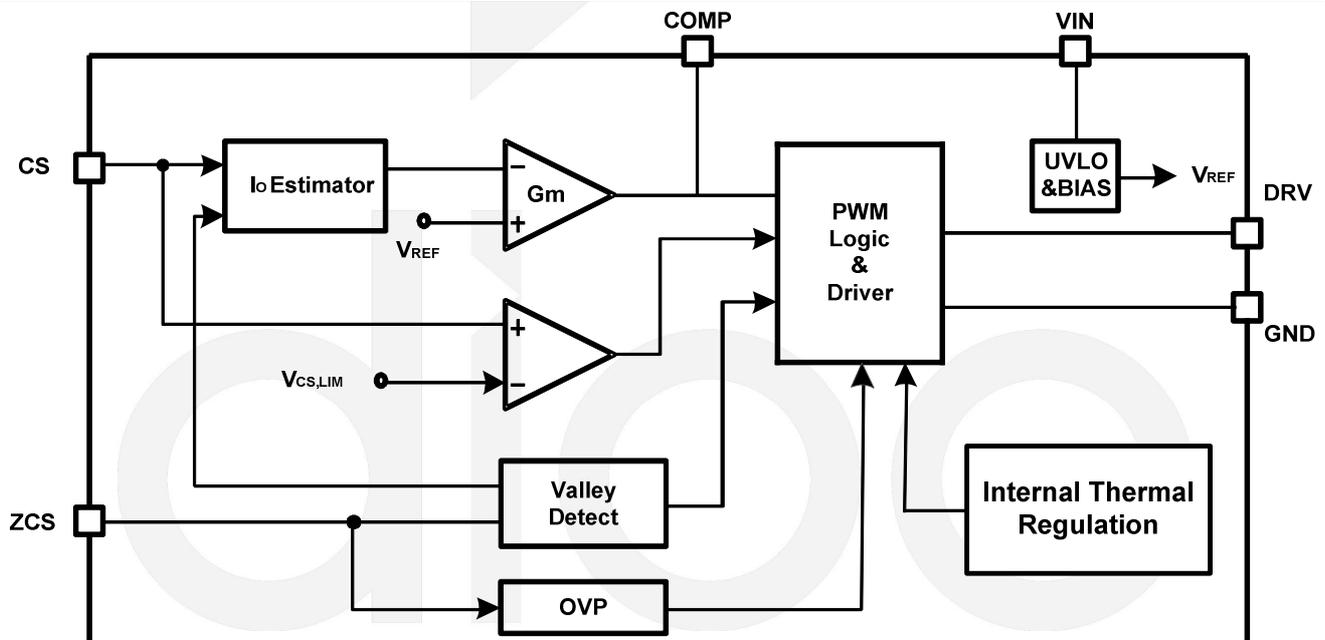- **Down light**
- **Tube lamp & PAR lamp &Bulb lamp**

Block Diagram

Figure1 Block Diagram

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Ordering Information

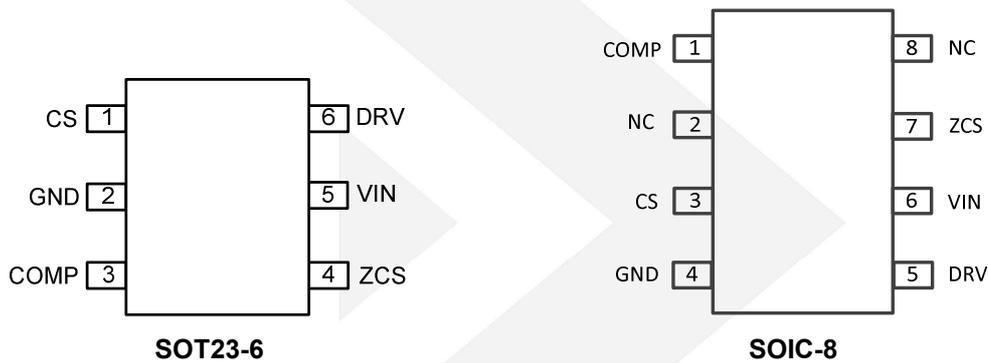| Order Part Number	Top Marking		T_A	Package	
DIO8850BCST6	YW85	Green	-40 to 85°C	SOT23-6	Tape & Reel, 3000
DIO8850BCS8	DIO8850B	Green	-40 to 85°C	SOIC-8	Tape & Reel, 2500

Pin Assignments

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        SOT23-6                              SOIC-8
                                   COMP  1        8  NC
  CS  1          6  DRV            NC    2        7  ZCS
  GND 2          5  VIN            CS    3        6  VIN
  COMP 3         4  ZCS            GND   4        5  DRV
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SOT23-6   **SOIC-8**

Figure 2 Pin Assignment (Top View)

Pin Definitions

Pin Name	Description
CS	Current sense pin. Connect this pin to the source of the primary switch. Connect the sense resistor across the source of the primary switch and the GND pin. (current sense resister Resistor: $Rs = \dfrac{1}{2} \times \dfrac{Vref}{Iout}$).
GND	Ground pin.
COMP	Loop compensation pin. Connect a RC network across this pin and ground to stabilize the control loop.
ZCS	Inductor current zero-crossing detection pin. This pin receives the auxiliary winding voltage by a resister divider and detects the inductor current zero crossing point. This pin also provides over voltage protection and line regulation modification function simultaneously. If the voltage on this pin is above $V_{ZCS,OVP}$, the IC would enter overvoltage protection mode. Good line regulation can be achieved by adjusting the upper resistor of the divider.
VIN	Power supply pin. This pin also provides output over voltage protection along with ZCS pin.
DRV	Gate driver pin. Connect this pin to the gate of primary MOSFET.
NC	Not Connect.

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Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Rating" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Parameter		Rating	Unit
VIN, DRV		-0.3 to 19	V
Supply Current I_{VIN}		15	mA
ZCS		-0.3 to VIN+0.3	V
CS, COMP		-0.3 to 5	V
Power Dissipation, P_D @ T_A = 25°C, SOT23-6		0.6	W
Package Thermal Resistance, SOT23-6	Θ_{JA}	170	°C/W
	Θ_{JC}	130	
Power Dissipation, P_D @ T_A = 25°C, SOIC-8		1.1	W
Package Thermal Resistance, SOIC-8	Θ_{JA}	100	°C/W
	Θ_{JC}	50	
Storage Temperature Range		-65 to 150	°C
Junction Temperature Range		150	°C
Lead Temperature Range		260	°C
ESD	HBM, JEDEC: JESD22-A114	2000	V
	MM, JEDEC: JESD22-A115	200	

Recommend Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended Operating conditions are specified to ensure optimal performance to the datasheet specifications. DIOO does not Recommend exceeding them or designing to Absolute Maximum Ratings.

Parameter	Rating	Unit
Supply Voltage	9.5 to 15.4	V
Junction Temperature Range	-40 to 125	°C
Ambient Temperature Range	-40 to 85	°C

Electrical Characteristics

V_{IN} = 12V, T_A = 25°C, unless otherwise specified.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
$V_{VIN,ON}$	VIN turn-on threshold			16		V
$V_{VIN,OFF}$	VIN turn-off threshold			7.3		V
$V_{VIN,OVP}$	VIN OVP voltage			17.8		V
I_{ST}	Start up Current	$V_{VIN} < V_{IN,OFF}$		15		µA
I_{VIN_OVP}	Shunt current in OVP mode	After $V_{VIN} > V_{VIN,OVP}$, then set VIN=15V		2		mA
I_Q	Quiescent Current	No switching		0.26		mA
$I_{VIN.}$	Operation current	C_L=100pF,f=15kHz		0.36		mA
Error Amplifier Section						
V_{REF}	Internal reference voltage		0.294	0.3	0.306	V
ZCS pin Section						
$V_{ZCS,OVP}$	ZCS pin OVP voltage threshold			1.5		V
V_{FAST_ST}	Fast start up threshold			0.55		V
Current Sense Section(Source PIN of integrated MOSFET)						
$V_{CS.LMT}$	Current limit reference voltage			0.75		V
K_2	CC Feed forward coefficient			0.1		
R_{K2}	CC Feed forward resistor R_{K2}			1020		Ω
PWM Section						
$T_{ON.MAX}$	Max on time	COMP high clamp		27.15		µs
T_{LEB}	Leading edge blanking time			350		ns
$T_{OFF,MAX}$	Max OFF time			68		µs
$T_{LEB.ZCD}$	ZCD Blanking Time	After turn off		2		µs
f_{MAX}	Maximum switching frequency			115		kHz
Gate Driver						
V_{GATE_CLAMP}	Output Clamp Voltage	V_{CC}=20V		12		V
T_{RISING}	rising time from 10% to 90%	C_{LOAD}=1nF		210		ns
$T_{FALLING}$	falling time from 90% to 10%	C_{LOAD}=1nF		40		ns
Thermal Section						
TSD	Thermal Shutdown Temperature			150		°C

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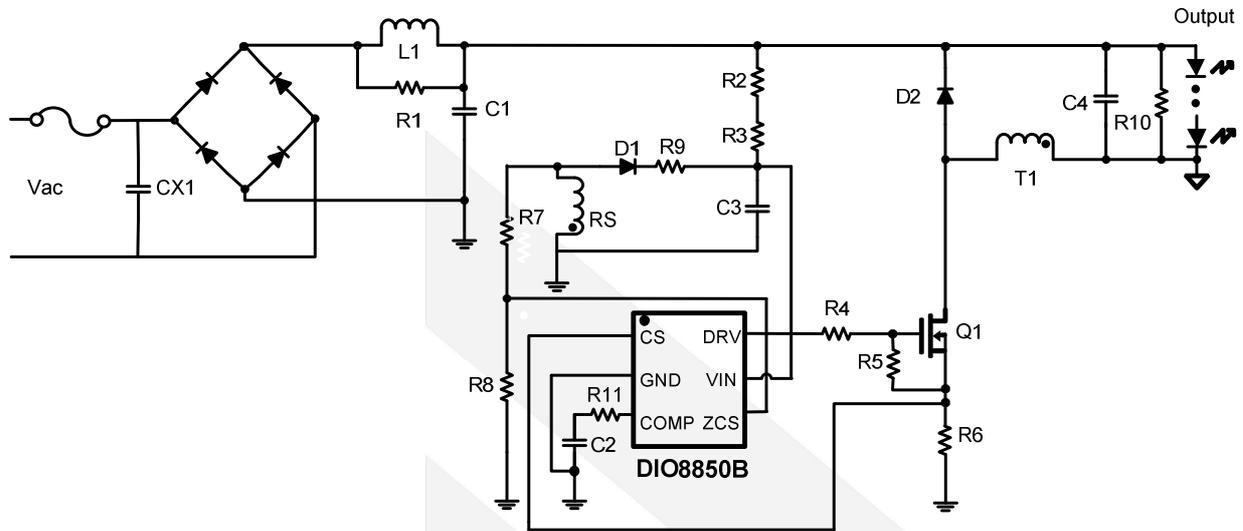
Typical Application

Figure 3 Typical Application

Application Information

Start up

After AC or DC supply is powered on, the capacitor C_{VIN} across VIN and GND pin is charged up by BUS voltage through a start up resistor R_{ST}. Once V_{VIN} rises up to V_{VIN-ON}, the internal blocks start to work and PWM output is enabled.

Shut down

After AC supply or DC BUS is powered off, the energy stored in the BUS capacitor will be discharged. When the auxiliary winding of transformer can't supply enough energy to VIN pin, V_{VIN} will drop down. Once V_{VIN} is below $V_{VIN-OFF}$, the IC will stop working and V_{COMP} will be discharged to zero.

Quasi-Resonant Operation

QR mode operation provides low turn-on switching losses for Buck converter.

Over Voltage Protection (OVP) & Open LED Protection (OLP)

The output voltage is reflected by the auxiliary winding voltage of the transformer, and both ZCS pin and VIN pin provide over voltage protection function. When the load is null or large transient happens, the output voltage will exceed the rated value. When V_{VIN} exceeds $V_{VIN,OVP}$ or V_{ZCS} exceeds $V_{ZCS,OVP}$, the over voltage protection is triggered and the IC will discharge V_{VIN} by an internal current source $I_{VIN,OVP}$. Once V_{VIN} is below $V_{VIN,OFF}$, the IC will shut down and be charged again by BUS voltage through start up resistor. If the overvoltage condition still exists, the system will operate in hiccup mode.

Short Circuit Protection (SCP)

When the output is shorted, the output voltage is clamped to zero. The voltage of the auxiliary winding is proportional to the output winding, so V_{VIN} will drop down without auxiliary winding supply. Once V_{VIN} is below

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$V_{VIN,OFF}$, the IC will shut down and be charged again by the BUS voltage through the start up resistor. If the short circuit condition still exists, the system will operate in hiccup mode. In order to guarantee SCP function not effected by voltage spike of auxiliary winding, a filter resistor is needed (10Ω typically).

CONTACT US

Dioo is a professional design and sales corporation for high-quality and performance analog semiconductors. The company focuses on industry markets, such as, cell phone, handheld products, laptop, and medical equipment and so on. Dioo's product families include analog signal processing and amplifying, LED drivers and charger IC. Go to http://www.dioo.com for a complete list of Dioo product families.

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